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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10050104	FILING DATE 01/18/2002	CLASS 438	SUBCLASS 217	GAU 2812	EXAMINER Bratlie
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**APPLICANTS: Choi Chul-Hwan; Kim Yong-Gab; Cho Chan-Hyung;

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**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

REPUBLIC OF KOREA 2001-25078 05/09/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials		ATTORNEY DOCKET NO SEC.890
TITLE : Self-contained semiconductor device manufacturing equipment having compact arrangement of load-lock and processing chambers		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg.
		Print Fig.	
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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